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| | | | | Application Number | Not assigned | A.C.I | |
| INF | ORMATIO | N D | ISCLOSURE | Filing Date | Herewith | .2. | |
| STATEMENT BY APPLICANT | | | | First Named Inventor | James Kardach | s.6 | |
| 017 | / 1 P=141 p=14 1 | . | AI I LIOANI | Group Art Unit | Not assigned | 50 | |
| (use as many sheets as necessary) | | | | Examiner Name | Not assigned | 9,1 | |
| Sheet | 1 | of | 1 | Attorney Docket Number | 042390.P6596 | <u> </u> | |

| OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS | | | | | | | |
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| Examiner Initials* | Cite No.1 | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | | | | | |
| M | 1 | "Intel Announces Industry's First Single-Chip Silicon Solution For Home Networking" www.intel.com/pressroom/archive/releases/Hn091598.htm. May 4, 2000 | | | | | |
| | | "Intel AnyPoint Home Network" www.intel.com/anypoint/index.htm. May 4, 2000 | | | | | |
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